

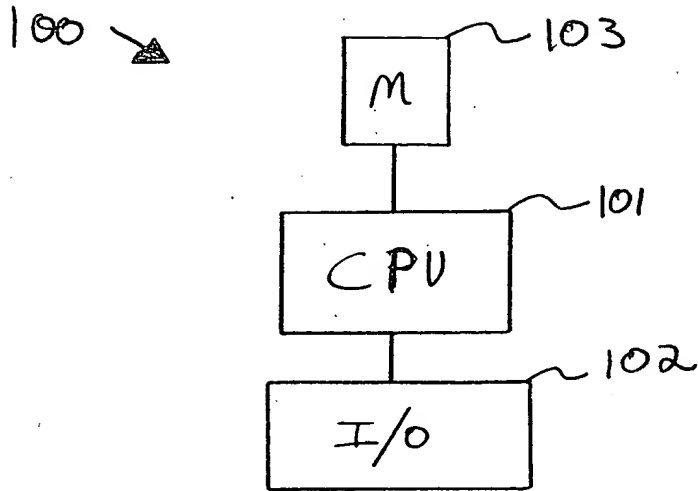
[illegible]

FIG. 1

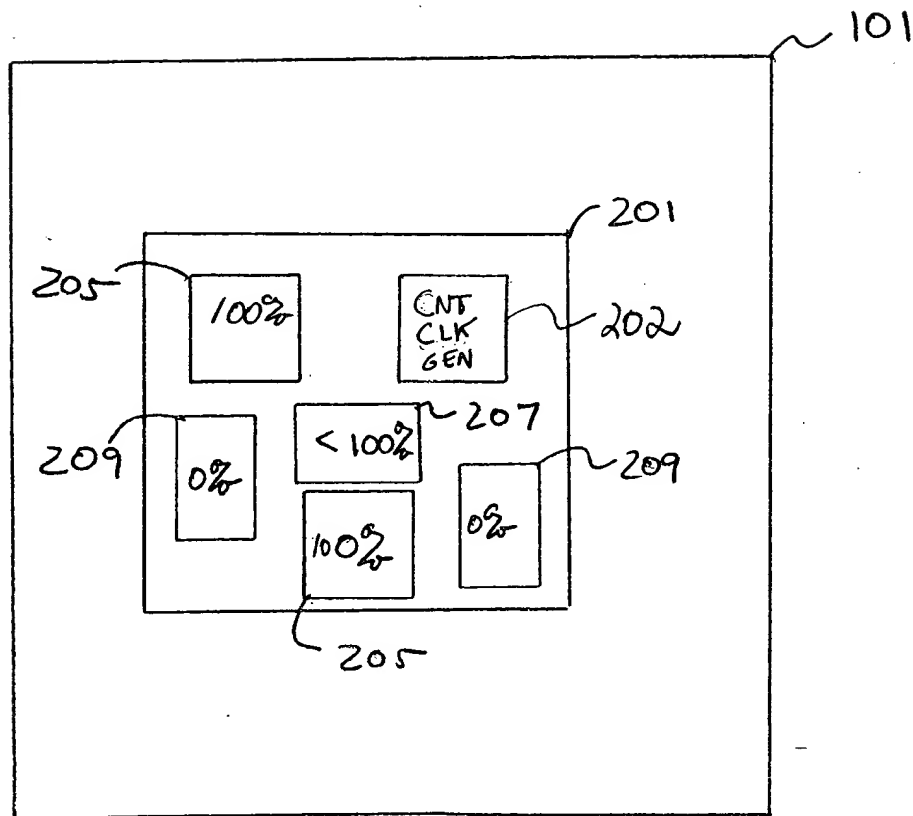


FIG. 2

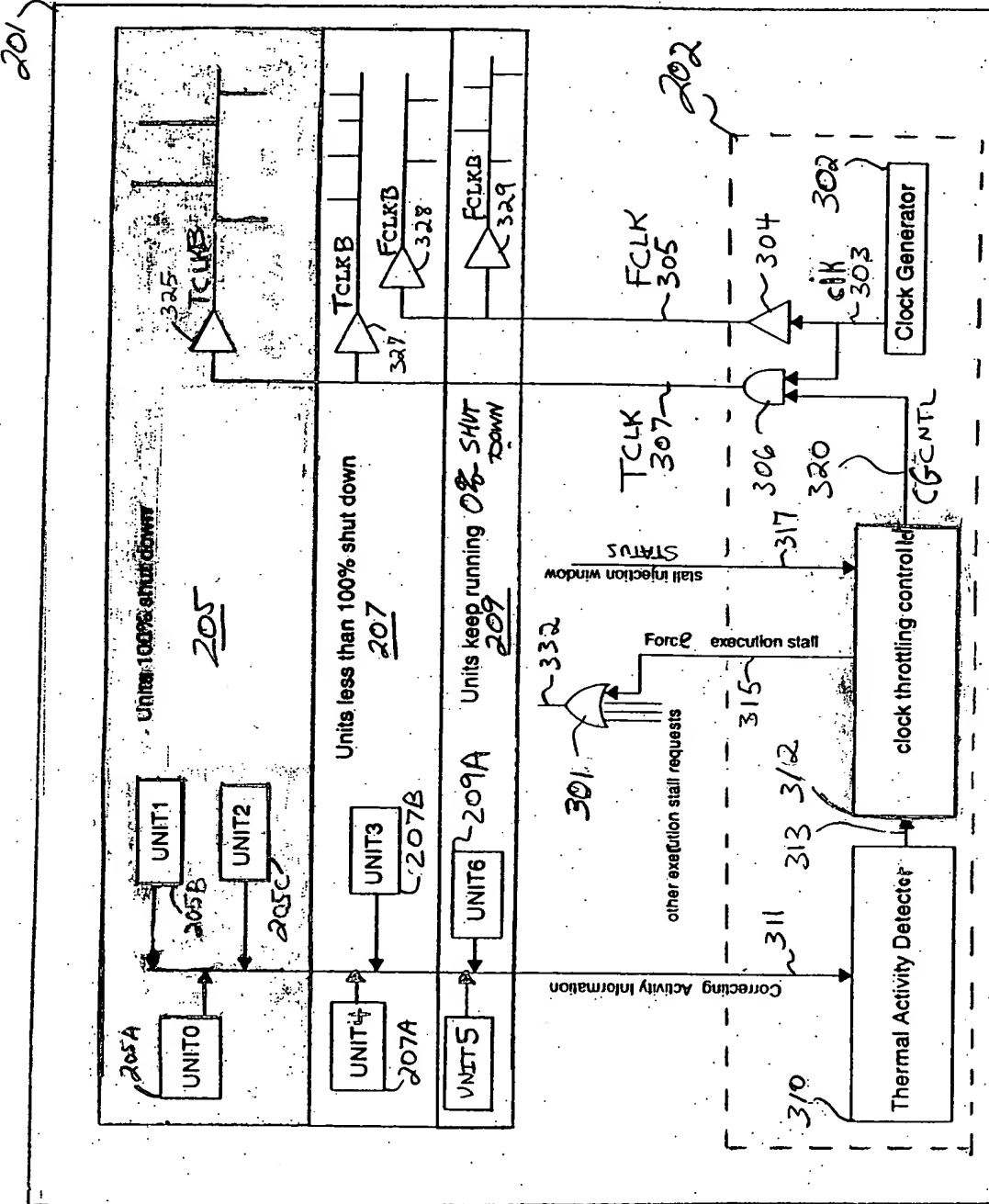


FIG. 3

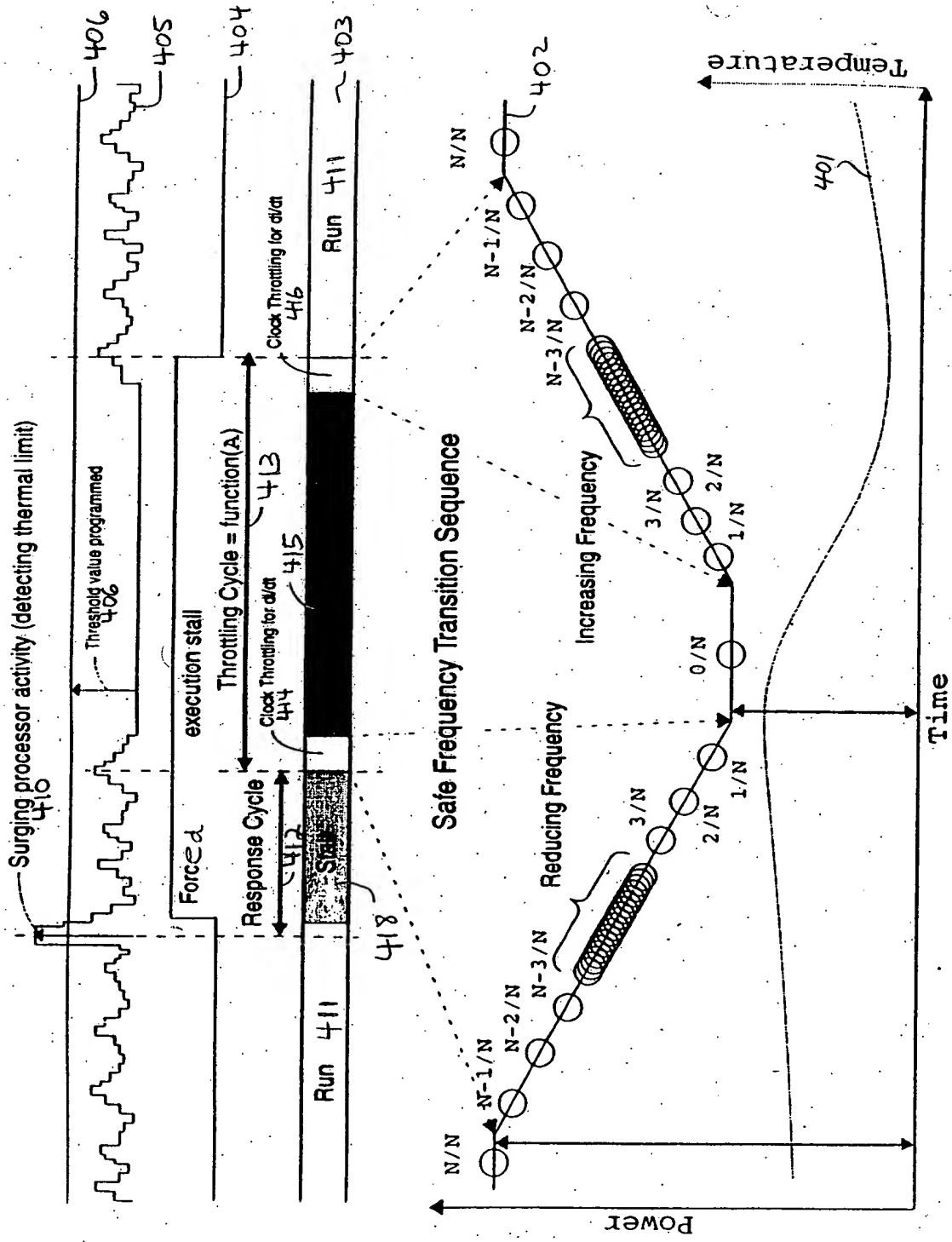


FIG. 4

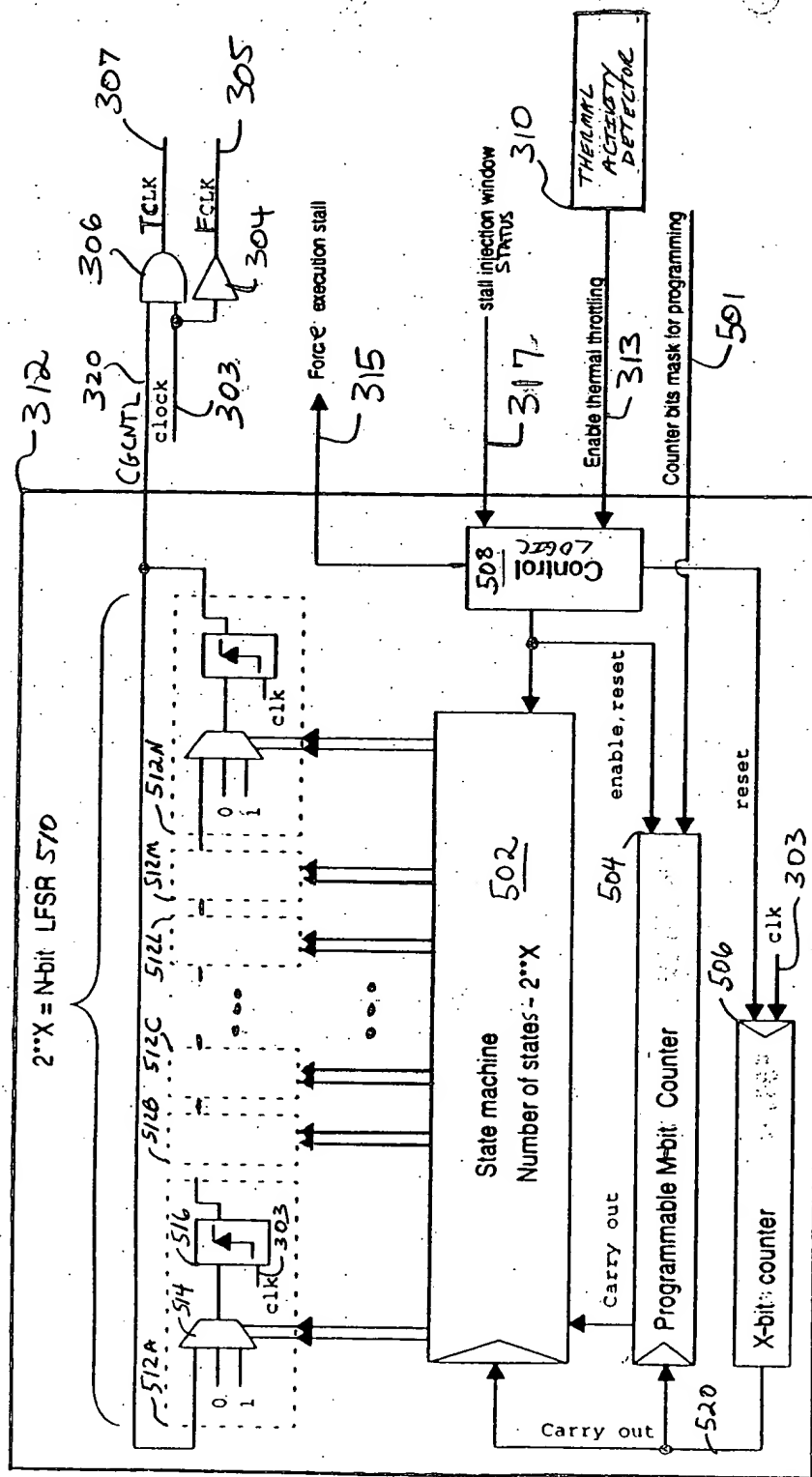
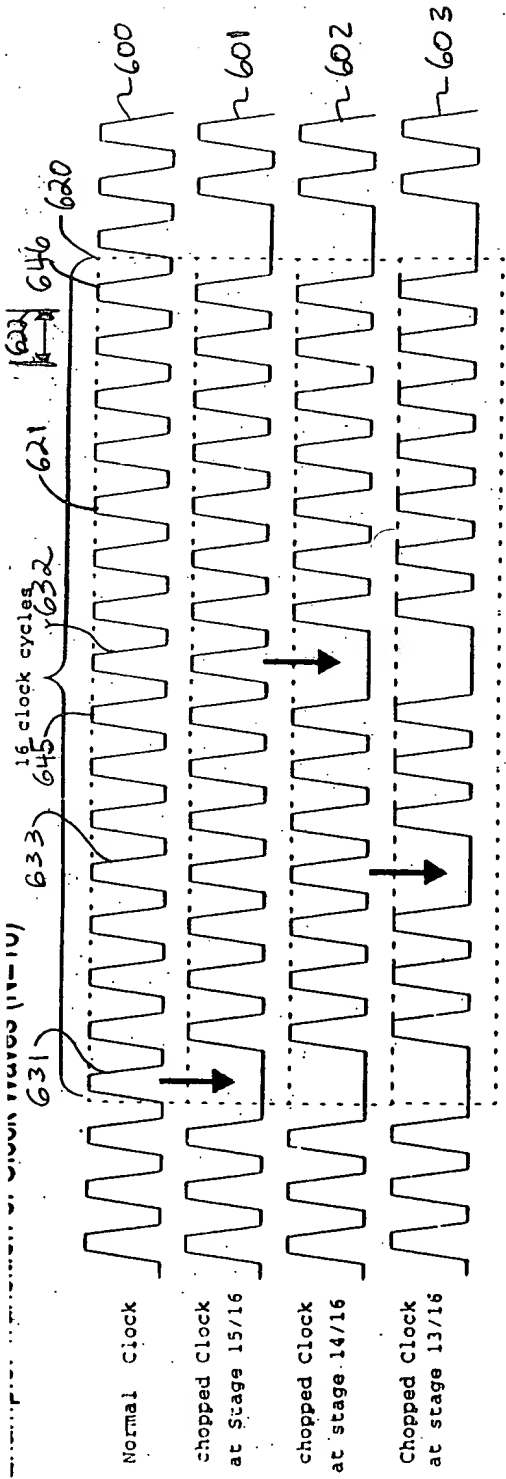


FIG. 5



• Clock wave chopped one by one at each stage  
 • Keep enough interval time from one stage to another to relax di/dt.  
 •

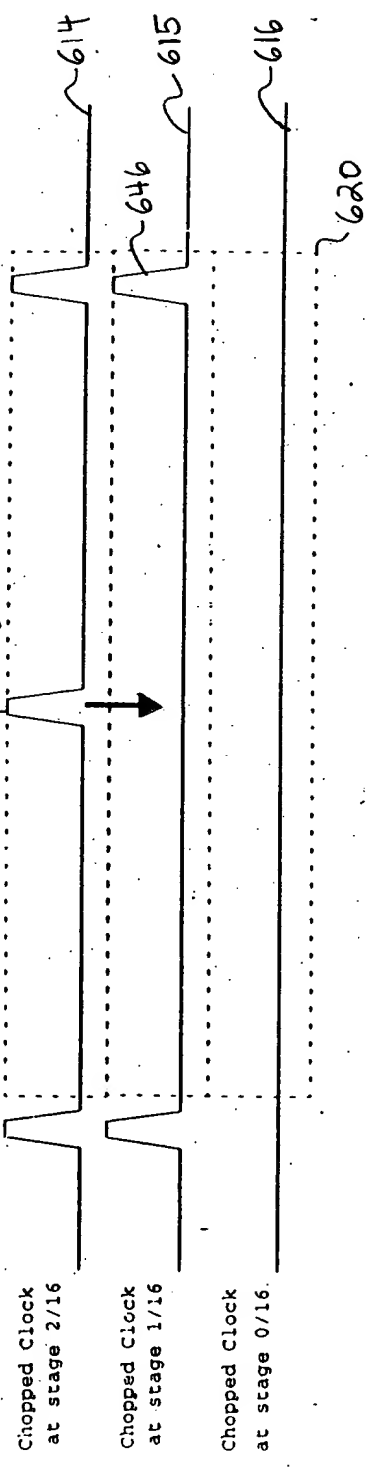


FIG. 6

